

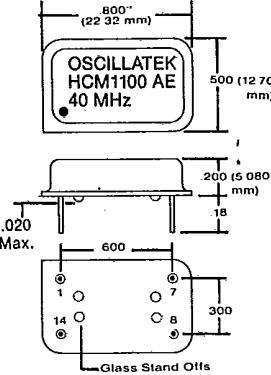
HIGH SPEED C-MOS  
HCM1100 SERIES — 4 PIN DIP PACKAGE

# CRYSTAL CLOCK OSCILLATORS

T-50-23

	<b>OPTIONS</b> <ul style="list-style-type: none"> <li>• OUTPUT LOGIC • FREQUENCY • STABILITY</li> <li>• TEMPERATURE RANGE • SUPPLY VOLTAGE</li> </ul>
	<h3>SPECIFICATIONS</h3> <p><b>OUTPUT:</b> HIGH SPEED C-MOS</p> <p><b>OPERATING TEMP. RANGE:</b> 0°C to +70°C</p> <p><b>STORAGE TEMP. RANGE:</b> -55°C to 125°C</p> <p><b>SUPPLY VOLTAGE:</b> 5.0 VDC, ± 10%</p> <p><b>SUPPLY CURRENT:</b> 60 mA MAX. @ 50 MHz 45 mA MAX. @ 30 MHz 30 mA MAX. @ 20 MHz 20 mA MAX. @ 10 MHz</p> <p><b>DUTY CYCLE:</b> 60/40%, at the 50% level</p> <p><b>Tr, Tf:</b> 5.0nS MAX, 10% to 90% Levels</p> <p><b>Voh:</b> Vcc-0.2V MIN</p> <p><b>Vol:</b> 0.2 V, MAX.</p>

PIN CONNECTIONS	
1	SEE OPTION CHART
7	GND / CASE
8	OUTPUT
14	Vcc



\*.265" max. for frequencies above above 100 MHz

### ORDERING METHOD

STANDARD SERIES—HCMOS	ABSOLUTE STABILITY	OUTPUT OPTION					—	FREQUENCY
HCM1100	± .01%	OPT. #	DESCRIPTION	PIN 1 FUNC.	PIN 1	PIN 8	—	60 Hz to 50 MHz
HCM1114	± .05%	A	STANDARD	N.C.	N.C.	S S S	—	60 Hz to 50 MHz
HCM1115	± .1%	AD**	DUAL PHASE	OUTPUT	S S S	S S S	—	60 Hz to 50 MHz
HCM1144	± .0025%	AE**	ENABLE	INPUT	—	S S	—	60 Hz to 50 MHz
HCM1145	± .005%	AF**	DUAL FREQ. ***	OUTPUT	—	S S S	—	60 Hz to 50 MHz
		AZ**	TRI-STATE	INPUT	—	S S .....	—	60 Hz to 50 MHz

### EXAMPLE

HCM1100	AE	—	40 MHz	Write "Screened" if screening to MIL-0-55310/16, Class B, Table II is required
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NOTE: HCM1100AE-40.000000 MHz is a model number in above example selected with HC-MOS compatible output in 4-pin DIP package with glass stand offs, standard Pin Out, ± .01% stability over 0°C to 70°C, and output disable capability.

\*\*Not available above 50 MHz.

\*\*\*Pin 1 freq. is binarily derived from the pin 8 freq.